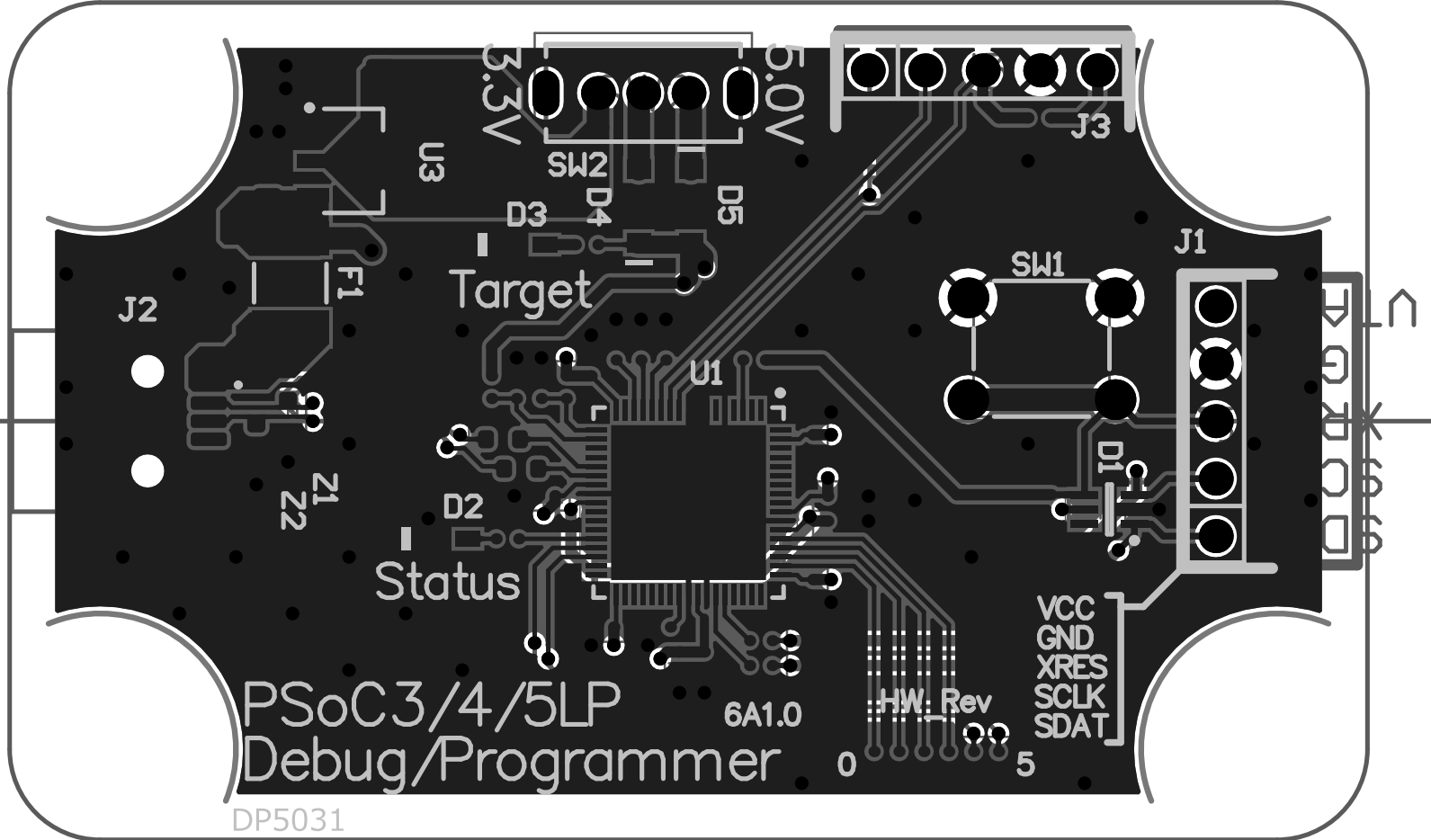


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PSoc3/4/5LP Programmer		
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PSoC3/4/5LP
Debug/Programmer

DP5031

6A1.0

HW Rev

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VCC
GND
XRES
SCLK
SDAT

5.0V
3.3V

Target

Status

J2

SW1

J1

J3

U3

SW2

D3

D4

D5

Z1
Z2

D2

U1

F1

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GND

VCC

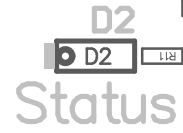
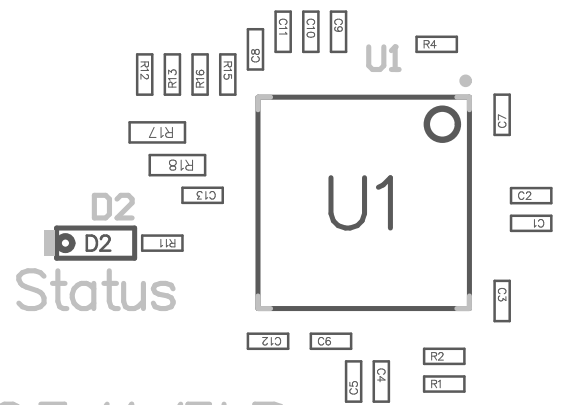
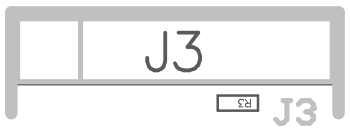
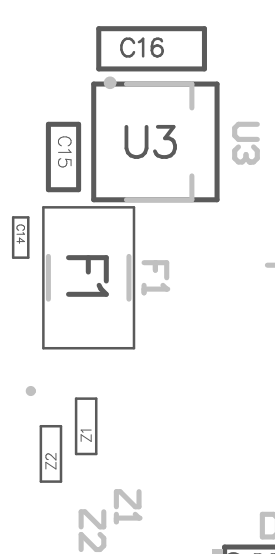
XRES

SCLK

SDAT

J2

J2



PSoC3/4/5LP
Debug/Programmer

6A1.0

HW_Rev



DP5031

